

09/830290

603 Rec'd PCT/PTO

25 APR 2001

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6/21

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Janez PIRŠ et al.

Appl. No. Not Yet Assigned

Confirmation No. Not Yet Assigned

Filed: Concurrently

Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

Atty. Docket No.: 38787-171294

For: DRIVING SCHEME AND  
ELECTRONIC CIRCUITRY FOR  
THE LCD ELECTRO-OPTICAL  
SWITCHING ELEMENT

Customer No.:



26694

**Preliminary Amendment**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

With reference to the claims as amended at the International stage which are annexed to the International Preliminary Examination Report, and which are to be used for purposes of the U.S. examination, please amend the claims as follows:

4. (Amended) The electronic circuitry for the implementation of the driving scheme as claimed in claim 3, characterized in that it reduces the time-interval variations of the polarity change of the electric driving signals by means of using the additional analog switch (24) that selects between the voltage levels  $V_{S1}$  and  $V_{S2}$ , connected to the inputs (27) and (28) of the said analogue switch so that it changes the reference voltage  $V_{C1}$  at its output (25), connected to the reference input (21) of the comparator (20) and

that the selection of the reference voltage is made synchronously with the adequately selected driving signal for the LCD electrooptic switching element (1) and according to the signal given by the sensor element (35) so that the signal, which is generated by the sensor (35) at its output (34), connected to the synchronization input (31), synchronizes the logic control circuitry (30) in such a way that the logic signal at its output (32), connected to the control input (26) of the analogue switch (24), controls the said analogue switch in such a way that it selects the voltage level  $V_{C1}$  at its output (25), connected to the reference input (21) of the comparator (20), so that the time-interval variations of the